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Avnet Electronics Marketing Asia Releases Actel ProASIC3 Evaluation Kit

Singapore -- Avnet Electronics Marketing Asia, an operating group of Avnet, Inc. (NYSE: AVT), today announced the release of its Actel ProASIC3 Evaluation Kit, the only available solution that enables product development and evaluation using the highest density ProASIC3 field-programmable gate array (FPGA). Featuring a single-chip, one million gate A3P1000 device in a 484-pin FPGA package, the new kit provides customers with a low-cost solution to validate the performance of the high-density, kit-compatible A3P400, A3P600 and A3P1000 ProASIC3 devices. For designers of power-sensitive portable applications, the kit also enables quick prototyping of the new flash-based Actel IGLOO family, the industry's lowest power FPGAs.

Avnet's new evaluation kit allows flexible implementation options for the up to 288 user I/Os, including high-speed, single-ended and differential pairs, and provides more than 50 uncommitted general-purpose I/Os brought to header pins. Additionally, expansion bus signal integrity is maintained by using high-speed Q-strip connectors that support daughter cards designed to Avnet's EXP(TM) expansion module specification.

The Actel ProASIC3 Evaluation Kit contains a single-chip A3P1000 FPGA, power supply, tutorials and support documentation in addition to the board. Additional support is available from Actel, including a high-speed FlashPro3 programmer, which interfaces to a PC via a USB 2.0 or USB 1.1 interface, and Actel's Libero Integrated Design Environment (IDE) software, which can be downloaded from Actel's Web site at www.actel.com.

"Since its introduction, Actel's ProASIC3 family has seen great success in the market. With the continued global interest in flash-based FPGAs and the recent introduction of Actel IGLOO, the roll out of Avnet's ProASIC3 Evaluation Kit is a timely one," said Martin Mason, director of silicon marketing, Actel. "The combination of Actel's low-cost, high-density ProASIC3 FPGAs and a customized development kit enables designers to reduce time to market without compromising on cost or performance."

"To meet with customers' stringent product requirements, OEMs nowadays are looking for higher density FPGAs while delivering more functionality," said Andy Wong, senior director, segment marketing and design services, for Avnet Electronics Marketing Asia. "With Actel ProASIC3 Evaluation Kit, Avnet is expanding options, including density and pin count, for designers using flash-based FPGAs. Combined with our value-added technical support, customers now can bring their products to market sooner while enjoying the cost effectiveness."

The kit is available for immediate purchase with delivery in mid-October. For more information, please visit <http://em.avnet.com/proasic3evalkit>.

About Avnet Electronics Marketing

Avnet Electronics Marketing Asia is part of the largest operating group of Phoenix-based Avnet, Inc. (NYSE: AVT), a Fortune 500 company with fiscal 2006 sales of USD\$14.25 billion. Serving customers in approximately 70 countries, Avnet is one of the world's largest technology marketing, distribution and services companies.

Avnet Electronics Marketing has a significant presence in Asia-Pacific -- the fastest growing electronics market in the world. Headquartered in Singapore, the company has 38 locations in 10 countries in Asia. It distributes semiconductors, interconnect, passive and electromechanical components to serve a wide range of customers including original equipment manufacturers (OEMs), electronic manufacturing services (EMS) providers, and small- to medium-sized businesses, and provides associated design-chain and supply-chain services. The company's web site is located at www.em.avnet.com

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